

Micropatterning in bistable cholesteric device with Bragg's reflection

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Abstract. In this work, the method to form bistable patterning in a cholesteric cell with Bragg's reflection in the visible spectral range is demonstrated. To reach that, we used a Si/SiO₂ structure in which a potential relief on SiO₂ surface is induced due to enrichment and depletion of required form areas of the silicon near-surface layer during external electric field action. The patterning of enrichment is determined by the positive charge embedded into SiO₂ film, which is formed by thermodiffusion of aluminum atoms.

Keywords: liquid crystal, selective reflection, silicon, patterning electrode.

1. Introduction

Cholesteric liquid crystals (CLCs) possessing Bragg's reflection in the visible spectral range are utilized in a variety of polarizerless bistable displays [1, 2]. There are also a good perspective of application of Bragg's cholesterics in electrically driven diffraction gratings, dye doped LC lasing with periodic patterning. In the latter case, a flat phase-locked laser can be developed.

In order to realize unique properties of Bragg's cholesterics in applications, it is necessary to develop an effective micropatterning of the LC layer. Such methods of surface treatments as photoalignment [3, 4], ion bombardment [5], AFM scanning microscopy technique [6], tilted plasma [7], self-assembled gels and networks [8] give good results for alignment and patterning the nematics or cholesterics having large helix pitch but absolutely unsuitable for patterning the Bragg cholesteric with reflection in the visible spectral range.

In currently Bragg devices, a conventional indium-tin oxide (ITO) electrode patterning is used. However, ITO patterning has some disadvantages associated with the appearing relief. The ITO surface relief can play a role of a source of light wave diffraction that impairs particularly a performance of diffraction grating, LC wave guides, lasing in LCs.

It is well known [9, 10] that Si/SiO₂ structures are often used as a photosensitive part of liquid crystal spatial light modulators (LCSLM). A photosensitive state of Si/SiO₂ structure is achieved using the external electric field due to the so-called field effect. In this state, there is a depletion of major carriers within the

near-surface layer of silicon, and, accordingly, the electric resistivity of this layer increases. The Si/SiO₂ structure transforms the written light distribution to the potential relief on the SiO₂ surface owing to non-equilibrium carrier generation within the depleted layer. In accordance with this potential relief a LC director reorients and modulates reading light in LCSLM.

SiO₂ dielectric film in the Si/SiO₂ structure performs at least two functions. First, the SiO₂ film prevents charge carriers to transfer from LC into silicon that enables to form a depleted layer. Second, this SiO₂ film plays a role of a dielectric mirror which reflects reading light in LCSLM.

This work shows that the issue of reliefless patterning for a cholesteric cell with Bragg's reflection can be solved by means of using the Si/SiO₂ structure with an embedded quasi-electrode within the SiO₂ film. The ion charge formed in the SiO₂ film due to thermodiffusion of aluminum atoms plays a role of a gate that permanently enriches the silicon near-surface layer. During control pulse action, the patterned quasi-electrode prevents depletion of the silicon near-surface layer, whereas in the rest part of silicon there observed an electron depletion.

2. Experiment

In our experiment, as a Si/SiO₂ structure we used a silicon substrate of the *n*-type with the specific resistance 4.5 Ohm-cm containing *p*⁺-pockets. On the silicon surface, the dielectric amorphous SiO₂ film was thermally grown with the thickness of 0.4 μm.

An embedded quasi-electrode formation and its micropatterning within SiO₂ film were made in the following way. On the silicon oxide film, the aluminum film of about 0.7- μm thickness was thermally deposited. Using photolithographic and selective etching processes, the aluminum film was patterned on the SiO₂ surface as shown in Fig. 1a. Usually, the thermal annealing of the aluminum film on silicon oxide results in appearance of alumino-silicate nanoscale islands on the SiO₂ surface or its change at the nanoscale level [11, 12]. In order to avoid any surface changes at the nanolevel, the Si/SiO₂ structure was annealed at the temperature lower than that necessary to obtain a good adhesion of aluminum to silicon oxide [13]. After annealing, the patterned aluminum film was removed by selective etching (Fig. 1b).

The investigation of the SiO₂ surface by using an optical microscope showed that "A" and "B" type of surfaces are optically identical (Fig. 1b).

Then, the SiO₂ surface was investigated using a scanning electron microscope (SEM). It was found that "A" areas look like darker than "B" areas (Fig. 2, "A" and "B" vertical areas). There takes place such darkening over both the *n*-type and *p*⁺-type conductivity silicon substrate surface (Fig. 2). "A" area darkening in comparison with "B" one means that the electron beam of SEM penetrates into the oxide film at a certain depth (~ 50 nm [14]) and additionally scatters by the defect layer which appears because of the aluminum ions are located here. So, due to thermodiffusion of aluminum atoms a static positive charge Q^+ is formed in the SiO₂ film, Fig. 1b. The shape and size of charge location exactly repeats the patterning of annealed aluminum film. Just under this positive charge there is an electron enrichment Q^- , Fig. 1b.

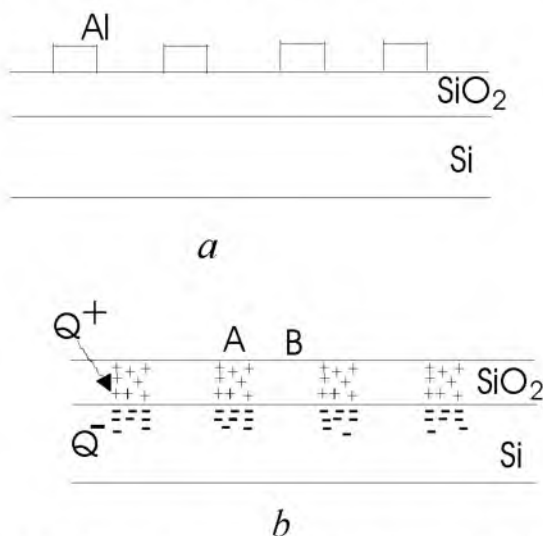


Fig. 1. Schematic representation of preparing the charge embedded into the Si/SiO₂ structure. a) Patterning the aluminum film on the SiO₂ surface. b) Formation of the embedded charge Q^+ (quasi-electrode) after annealing and selective wet etching of the aluminum film. Q^- – permanent electron enrichment. "A" and "B" types of surface are those from which aluminum film was removed after and before annealing, respectively. *p*⁺-pockets are not shown.

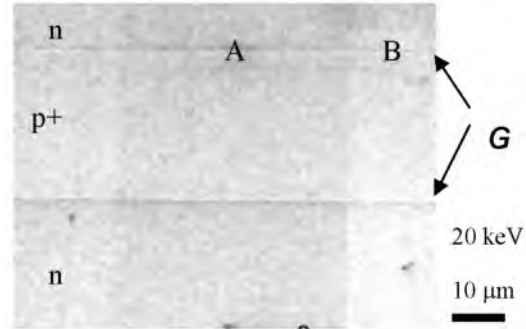


Fig. 2. SEM micrograph of SiO₂ surface on the *n*-type silicon substrate with *p*⁺-pocket after annealing and selective wet removing of aluminum pattern. Dark vertical area "A" is a track of the annealed aluminum film. "B" area is a surface from which the aluminum film was removed before annealing. "G" is the boundary between *p*⁺- and *n*-type conductivity areas.

The "A" and "B" surfaces were examined using an atomic force microscope (AFM, Digital Instruments NanoScope IIIa in the tapping mode) in the scanned areas of $1 \times 1 \mu\text{m}$. The surface topography in the points "A" and "B" as the height in 2D maps and their corresponding reliefs are shown in Fig. 3a, b. As it is well seen, the morphology of these fragments of "A" and "B" surfaces are almost identical. As seen, relief difference between these "A" and "B" surfaces is less than $1.5 \dots 2$ nm. This means that such a difference between the "A" and "B" relief cannot be a source of light wave diffraction in the visible range.

The LC cell was assembled using a Si/SiO₂ substrate treated in the above manner and an upper reference ITO substrate (1), which is coated with a rubbed polyimide film (2), as schematically shown in Fig. 4a. The cell was filled with the chiral (Fig. 4b) doped nematic (LC1268) with equilibrium helix equal ~ 400 nm having selective

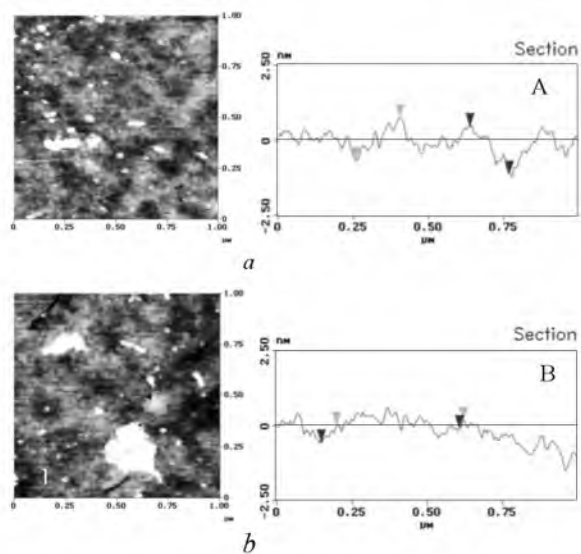


Fig. 3. 2D AFM images of "A"(a) and "B"(b) surfaces and their typical reliefs, respectively.

reflection at 625 nm in visible. Thickness of cholesteric layer was approximately equal to 1 μm .

Recording a pattern in such a cell with the cholesteric layer is carried out as follows. Simultaneously, a direct voltage with $+U$ -polarity on silicon and alternating one are applied to the cell. The DC voltage bias causes an electron depletion of silicon near-surface. Therefore, the depletion layer boundary (L) lowers (Fig. 4a) and the resistivity of the depleted layer increases. Here, a certain part of the external voltage drops on the depleted layer. On the other hand, just under the quasi-electrode, a silicon surface is not depleted because there is an initial electron enrichment (EE), Fig. 4a. Therefore, the external voltage applied to cell drops entirely on the cholesteric layer. Thus, there a potential relief forms on the SiO_2 surface which is identical to the quasi-electrode pattern. The potential relief difference between "A" and "B" areas runs up to several volts that is enough for transition into a focal conic texture over "B" areas and, at the same time, for planar \rightarrow homeotropic transition over "A" areas, Fig. 5a. Fig. 5b confirms that the silicon surface between quasi-electrodes is depleted and photosensitive. As well seen, the laser spot on "B" area causes a confocal \rightarrow homeotropic transition. This means that under the action of light irradiation the non-equilibrium electron-hole pairs are generated, and the near-surface silicon resistivity decreases here.

After switching off of the voltage, the homeotropic texture over "A" areas relaxes to the planar texture with Bragg's reflection while the cholesteric one over "B" area remains in the focal conic scattering texture (Fig. 6a). In order to erase the pattern, a high voltage has to be applied to switch a cholesteric texture over "B" areas to the homeotropic one. Then the cell completely relaxes back to the planar texture, Fig. 6b.

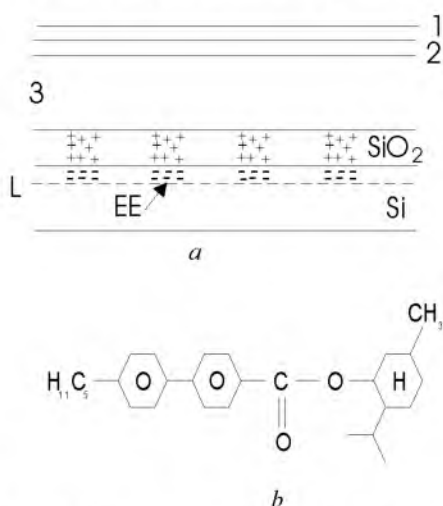


Fig. 4. a) Schematic view of the cholesteric cell based on the Si/SiO₂ structure. 1 – ITO electrode, 2 – rubbed polyimide layer for alignment of cholesteric, 3 – cholesteric layer, L – near-surface depletion boundary, EE – electron enrichment. b) Chiral component of cholesteric-nematic mixture.

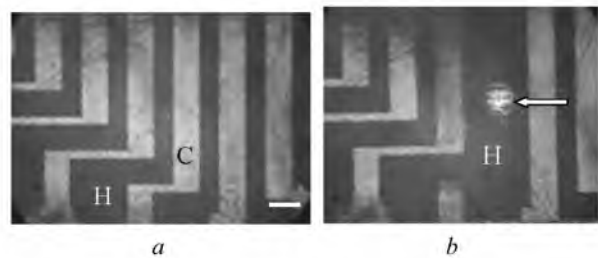


Fig. 5. a) Homeotropic (H) texture over quasi-electrodes ("A" areas) and focal conic (C) one over depleted areas of silicon ("B" areas). Frequency and AC voltage were 10⁵ Hz and 45 B, respectively. The DC voltage bias was + 3 V. The bar is 50 μm . b) Focal conic \rightarrow homeotropic transition under He-Ne laser illumination. The arrow points on the laser spot.

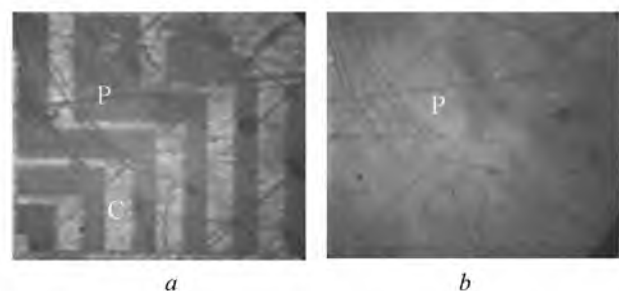


Fig. 6. a) Written patterning in a cholesteric layer. P – planar texture with Bragg's reflection, C – focal conic scattering texture. b) Erased pattern after high voltage pulse action.

It should be noted that the pattern is not associated with any surface contamination or a possible rest of aluminum film after etching. If the SiO_2 surface is etched in the layer-by-layer manner, the above described pattern can be recorded after each stage of etching and assembling of the cell. This confirms that the effect of patterning is associated with positive aluminum ion penetration and distribution inside SiO_2 from its surface to the silicon one during annealing.

3. Conclusion

Thermal diffusion of aluminum atoms from the aluminum pattern film stems from formation of the positive charge embedded into SiO_2 (quasi-electrode). This embedded charge faithfully copies the pattern of the aluminum film and permanently enriches the silicon near-surface layer in the Si/SiO₂ structure. Such Si/SiO₂ structures are used as a substrate in a cholesteric cell with Bragg's reflection. The potential relief on the SiO_2 surface is achieved when applying a pulse of the driving voltage to a cell. The "relief" amplitude is enough for simultaneous transitions of planar \rightarrow focal conic textures over depleted silicon and planar \rightarrow homeotropic ones over quasi-electrode, respectively. This enables to obtain

the rewritten patterned areas with Bragg's reflection in a cholesteric cell.

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